



(11) **EP 1 739 841 A1**

(12) **EUROPEAN PATENT APPLICATION**  
published in accordance with Art. 158(3) EPC

(43) Date of publication:  
**03.01.2007 Bulletin 2007/01**

(51) Int Cl.:  
**H03M 1/10** <sup>(2006.01)</sup> **G01S 7/03** <sup>(2006.01)</sup>  
**G01S 7/282** <sup>(2006.01)</sup> **G01S 13/34** <sup>(2006.01)</sup>  
**H03B 28/00** <sup>(2006.01)</sup>

(21) Application number: **05719419.3**

(22) Date of filing: **23.02.2005**

(86) International application number:  
**PCT/JP2005/002911**

(87) International publication number:  
**WO 2005/081407 (01.09.2005 Gazette 2005/35)**

(84) Designated Contracting States:  
**DE FR GB**

(30) Priority: **25.02.2004 JP 2004049586**

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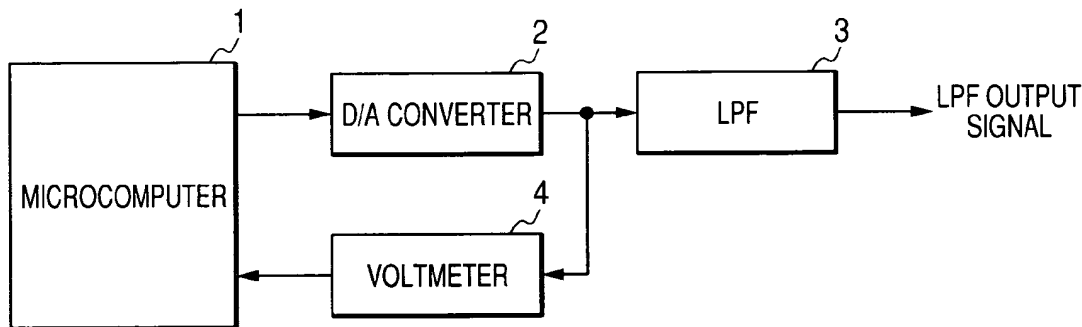
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(54) **WAVEFORM GENERATION METHOD, RADAR DEVICE, AND OSCILLATOR FOR RADAR  
DEVICE**

(57) It is possible to generate D/A conversion voltage in which an error generated by numeric irregularities of a D/A conversion element such as resistor constituting a D/A converter 11 is corrected. A waveform generation method **characterized in that** input data into a D/A converter 11 are provided to the D/A converter in order at a timing at which a voltage of a desired waveform which

has D/A conversion data indicating a conversion amount of the input data obtained by varying the input data by a minimum conversion unit or a unit obtained by multiplying the minimum conversion unit by an integer, and which varies with time series, becomes substantially equal to a D/A-converted voltage, whereby the D/A-converted voltage is generated in accordance with the desired waveform.

**FIG. 1**



**Description**

## Technical Field

5 **[0001]** This invention relates to a waveform generation method adapted to generate a waveform programmably, and a radar device provided with waveform generation means adapted to generate a waveform programmably.

## Background Art

10 **[0002]** In a conventional waveform generation method, output levels of a D/A converter are set at regular intervals, and a conversion timing is varied at a predetermined ratio, a desired signal is thereby obtained (for example, refer to the Patent Document 1).

In a conventional radar device, data obtained by correcting a linearity of an oscillating frequency of VCO occurring due to a non-linearity and the like of D/A converter, LPF, and VCO, which are provided between CPU and a voltage control oscillator (which will hereinafter be referred to as VCO), are provided to D/A converter at regular timing intervals (for example, refer to Patent Document 2).

**[0003]**

Patent Document 1: JP-A-61-144930 (page 3, left lower column, Fig. 3)

20 Patent Document 2: JP-A-2002-156447 (page 4, left column to page 5, right column, Fig. 1, Fig. 6)

## Disclosure of the Invention

## Problems that the Invention Is to Solve

25 **[0004]** When data are provided to a plurality of input terminals of D/A converter in a conventional waveform generation method, a voltage value subjected to D/A conversion by each input terminal is supposed to be constant. Therefore, an error of a voltage value occurring in each input terminal due to numeric irregularities of a D/A conversion element, such as a resistor etc. constituting the D/A converter and to be D/A converted is not taken into consideration. This caused the accuracy of a waveform to be deteriorated.

30 **[0005]** When conversion data including data on a D/A converter are measured in conventional radar device, the data inputted into the D/A converter are different from a minimum conversion unit, and values among the measured data are generated by interpolation based on approximation. Therefore, errors of D/A conversion voltage values generated in each input terminal due to numeric irregularities of the D/A conversion element, such as a resistor, etc. are not taken into consideration, and, therefore, the accuracy of a generated waveform was deteriorated. Since the accuracy of the generated waveform is deteriorated, the accuracy of the oscillation frequency of VCO oscillated on the basis of the generated waveform is also deteriorated. Therefore, in radar device for measuring a distance between the radar device and an object on the basis of a frequency reflected on the object, or a relative velocity with respect to the object, the measuring accuracy of the radar device was deteriorated. In order to improve the measuring accuracy, it is necessary to employ an expensive D/A having an increased bit number to cause a price of the radar device to become high.

40 **[0006]** The present invention has been made so as to solve these problems, and provides a waveform generation method and radar device which are capable of improving the accuracy of a waveform to be generated, even when a D/A conversion element, such as a resistor, etc. had numeric irregularities.

## 45 Means for Solving the Problems

**[0007]** In the waveform generation method according to the present invention, a voltage D/A converted in accordance with a desired waveform is generated on the basis of the D/A conversion data obtained by varying input data into the D/A converter by a minimum conversion unit, or by a unit obtained by multiplying the minimum conversion unit by an integer.

50 **[0008]** In the radar device according to the present invention, a voltage D/A converted in accordance with a desired waveform is generated on the basis of the D/A conversion data obtained by varying input data into the D/A converter by a minimum conversion unit, or by a unit obtained by multiplying the minimum conversion unit by an integer, and the voltage is provided to the oscillating means to thereby vary the oscillation frequency.

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## Advantage of the Invention

**[0009]** The present invention becomes able to generate a voltage in which an error occurring due to a scatter of the

numerical value of a D/A conversion element, such as a resistor constituting the D/A converter were corrected, by generating a D/A conversion voltage in accordance with a desired voltage on the basis of D/A conversion data obtained by varying input data into the D/A converter by a minimum conversion unit, or by a unit obtained by varying the minimum conversion unit by multiplying the minimum conversion unit by an integer.

Brief Description of the Drawings

[0010]

[Fig. 1] It is a block diagram of a waveform generation method showing an embodiment 1 of the present invention.  
 [Fig. 2] It is a waveform diagram describing an example of the generation of D/A conversion data showing the embodiment 1 of the present invention.  
 [Fig. 3] It is a waveform diagram showing an example of the generation of desired D/A input data shown in the embodiment 1 of the present invention.  
 [Fig. 4] It is a drawing of storage data in which desired D/A input data showing the embodiment 1 of the present invention are stored in an internal memory of a microcomputer 1.  
 [Fig. 5] It is a block diagram of a waveform generation method showing an embodiment 2 of the present invention.  
 [Fig. 6] It is a block diagram of waveform generation method showing an embodiment 3 of the present invention.  
 [Fig. 7] It is a block diagram of a FMCW radar device showing an embodiment 4 of the present invention.  
 [Fig. 8] It is a frequency variation diagram used to calculate a relative distance between the radar device and an object and a relative velocity thereof showing the embodiment 4 of the present invention.  
 [Fig. 9] It is a waveform of a desired waveform signal showing the embodiment 4 of the present invention.

Brief Description of the Drawings

[0011]

1 Microcomputer  
 2 D/A converter  
 3 LPF  
 4 Voltmeter  
 5 D/A conversion voltage signal  
 6 Desired waveform signal  
 7 Microcomputer  
 8 Voltmeter  
 9 IC tester  
 10 Microcomputer  
 11 VCO  
 12 Directional coupler  
 13 Transmission antenna  
 14 Mixer  
 15 Reception antenna  
 16 Amplifier  
 17 A/D converter  
 18 Transmission frequency  
 19 Reception frequency  
 20 Desired waveform signal

Best Mode for Carrying Out the Invention

Embodiment 1

[0012] Fig. 1 shows a block diagram of the waveform generation method in an embodiment 1 for carrying out this invention. In Fig. 1, the data (which will hereinafter be referred to as desired D/A input data) in accordance with a desired voltage waveform (which will hereinafter be referred to as desired waveform) stored in advance in a memory (not shown) contained in a microcomputer 1 are provided to a D/A converter 2, to generate a voltage (which will hereinafter be referred to as D/A conversion voltage) D/A converted in accordance with the desired waveform. This D/A conversion voltage is then passed through a low-pass filter LPF 3 to obtain a LPF output signal from which a high-frequency

component occurring with the D/A conversion is eliminated.

**[0013]** When the desired D/A input data are generated, the microcomputer 1 gives data (which will hereinafter be referred to as measured D/A input data) obtained by increasing in order by a minimum conversion unit of the D/A converter 2, or by a unit obtained by multiplying the minimum conversion unit by an integer, to the D/A converter 2, and an actual D/A conversion voltage is measured with a voltmeter 4 as a voltage measuring means to obtain data (which will hereinafter be referred to as D/A conversion data) corresponding to the D/A conversion voltage with respect to each measured D/A input data. A desired D/A input data are then generated by extracting the measured D/A input data at a timing at which a voltage of a desired waveform varying with time series and the D/A conversion voltage in the D/A conversion data become equal to each other.

**[0014]** The method of generating desired D/A input data in this embodiment will now be described with reference to Fig. 2 to Fig. 4. In the following description, measured D/A input data mentioned above are used as a minimum conversion unit of the D/A converter 2. This can also be applied to a case where the measured D/A input data are used as a unit obtained by multiplying the minimum conversion unit by an integer.

**[0015]** First, the method of generating the D/A conversion data will be described with reference to Fig. 2. Fig. 2 is a waveform diagram showing an example of the generation of D/A conversion data generated in advance of the generation of the desired D/A input data. In Fig. 2, the lateral axis represents measured D/A input data provided to the D/A converter 2, and the longitudinal axis D/A conversion voltage measured with the voltmeter 4. The data are shown with the input bit number of 8 of the D/A converter 2. The measured D/A input data is 00 (h) to FF (h) in hexadecimal notation, the D/A conversion voltage is 256 stages of V0 to V255.

**[0016]** The measured D/A input data are provided by the microcomputer 1 to the D/A converter 2 so that the data increase in order by one count from 00(h) to FF(h) to generate D/A conversion voltage signal 5. On each stage, a voltage of the D/A conversion voltage signal 5 is measured with the voltmeter 4 to obtain D/A conversion voltage V0 to V255.

**[0017]** The D/A converter 2 mentioned above generates an error of voltage to be D/A converted by each bit of an input terminal due to numeric irregularities of a D/A conversion element, such as a resistor etc. constituting the D/A converter 2. Therefore, the variation range of the D/A conversion voltage varying by each count of the D/A input data is measured as shown in Fig. 2 as the range varied in each count, or as the range varied periodically in accordance with the weight of each bit.

**[0018]** A method of forming desired D/A input data will be described with reference to Fig. 3 and Fig. 4. Fig. 3 is a waveform diagram showing an example of generation of desired D/A input data, and Fig. 4 is a drawing of storage data registered in the internal memory of the microcomputer 1. In Fig. 3, a lateral axis represents time, and a longitudinal axis represents a voltage. The longitudinal axis shows D/A conversion voltage V0 to V255 measured by the D/A conversion data generating method mentioned above. A timing t1 to t256 at which a desired waveform signal 6 shown with time series stored in advance in the internal memory in the microcomputer 1 crosses the D/A converted voltages V0 to V255 are shown on the lateral axis.

**[0019]** Each measured D/A input data at each timing of t1 to t256 can be obtained as desired D/A input data as shown in Fig. 4, by the above timing t1 to t256 and each measured D/A input data of 00(h) to FF(h) which generate each D/A conversion voltage of V0 to V255.

**[0020]** When the determination of the timing at which the desired waveform signal 6 crosses the D/A conversion voltage V0 to V255 is carried out in the microcomputer 1, a certain allowable value is provided with respect to each of the D/A conversion voltages, and a voltage value of the desired waveform stored in the internal memory is read in order in accordance with time series, a timing becomes able to be determined as the timing at which each of the D/A conversion voltages V0 to V255 is within the allowable value.

**[0021]** The voltmeter 4 was described as a part connected to the D/A converter 2 and the microcomputer 1. The voltmeter 4 may be provided so that the voltmeter 4 is released from the D/A converter 2 and microcomputer 1 after the D/A conversion voltage is measured during the generation of the desired D/A input data. The voltmeter 4 may be provided only when a D/A conversion voltage is measured, and may be removed from a product after the measurement is conducted.

**[0022]** The voltmeter 4 may also be made of an A/D converter.

**[0023]** The desired waveform signal 6 was shown as a signal increasing to a 256 stage of the D/A conversion voltage of V0 to V255 with time series. This signal may increase and decrease within the range in which the D/A converter 2 can convert the desired waveform signal 6. The number of bits of the D/A converter 2 may also be other than 8 bits at which the D/A conversion voltage is converted into 256 stages. An arbitrary D/A conversion voltage range may be set, for example, to a range having substantially equal to widths around V128 expressed as  $V128 \pm \Delta V$ , or V100 to V200 etc.

**[0024]** The microcomputer 1 may be formed so as to generate desired D/A input data corresponding to a desired waveform, and gives the data to the D/A converter 2. All or some of these actions may be made as individual functional circuits.

**[0025]** Thus, according to this embodiment, it becomes possible to generate a D/A conversion voltage in which an error occurring due to numeric irregularities of the D/A conversion element such as a resistor constituting the D/A

converter was corrected, by generating a D/A-converted voltage in accordance with the desired voltage on the basis of the D/A conversion data obtained by varying input data into the D/A converter 2 by a minimum conversion unit or by a unit obtained by multiplying the minimum conversion unit by an integer.

**[0026]** An example of the desired D/A input data outputted from the microcomputer 1 will now be described more in detail.

The microcomputer 1 outputs an input value (which will hereinafter be referred to as DAC input value), which designates D/A conversion voltage as desired D/A input data, into the D/A converter 2. The microcomputer 1 may store the timing (at which the D/A-converted voltage in the D/A conversion data and a voltage of a desired waveform varying with time series become substantially equal to each other) shown in Fig. 4 in the internal memory of the microcomputer 1.

For example, the voltage V0 shown in Fig. 2 is provided to the DAC input value 00, while the voltage V255 is provided to the DAC input value FF. The microcomputer 1 stores (the timing t0 corresponding to the DAC input value 00 is a timing 0) the timing t1 to t256 at which the DAC input values 01 (corresponding to the voltage V1) to FF (corresponding to the voltage V255) are inputted into the D/A converter 2 correspondingly to each of the DAC input value. The microcomputer 1 outputs the DAC input value to the D/A converter 2 in accordance with the stored data.

The D/A 2 outputs an analog signal providing a voltage corresponding to the DAC input value. For example, a voltage corresponding to the voltage V0 is outputted with respect to the DAC input value 00, and a voltage corresponding to the voltage V255 is outputted with respect to the DAC input value FF.

**[0027]** As the DAC input values, an initial value, the width of intervals of increment (or decrement) DAC input values and the number of increment (or decrement) are designated, and the DAC input value may be outputted at the corresponding timing.

For example, when as initial values, DAC input value is k, the width of intervals is 2 and frequency of occurrence of increment is n, a DAC input value k is outputted at a timing tk(=0), a DAC input value k+2 at the timing tk+2, a DAC input value k+4 at a timing tk+4, ... DAC input value k+2n at a timing tk+2n and so forth.

**[0028]** In another embodiment the microcomputer 1 may store different values for different temperatures correspondingly to desired D/A input data, and a timing for outputting a DAC input value may be computed at a predetermined temperature T.

For example, as the timing at which the DAC input value corresponds to the voltage Vk, the timing tk(T1) at which the temperature is T1 and the timing tk (T2) at which the temperature is T2 are obtained from the internal memory of the microcomputer 1. When tk-1(T1)=tk-1(T2) is set at the voltage of Vk-1, the timing tk(T) for outputting the output voltage Vk at the temperature T is expressed by the equation (1).

$$t_k(T) = (m_2/m) \cdot t_k(T_1) + (m_1/m) \cdot t_k(T_2) \dots \dots \dots (1)$$

wherein m=m1+m2; m1=T1-T and m2=T-T2.

The timing for outputting the DAC input value at the temperature of T corresponding to each voltage value may be determined in the same manner in order.

The microcomputer 1 outputs to the D/A converter 2 the D/A input value which is calculated on the basis of the timing of prescribed temperature in order the DA input value corresponding to the desired D/A input data.

#### Embodiment 2

**[0029]** Fig. 5 shows a block diagram of the waveform generation method in the embodiment 2 for carrying out this invention. In the above-described embodiment 1, the voltmeter 4 is connected to an output of the D/A converter 2. In this embodiment, the voltmeter 4 is connected to an output of LPF 3.

**[0030]** The operation of this embodiment is identical with that of the embodiment 1. It becomes possible to generate D/A conversion voltage in which the non-linearity and the like depending upon the voltage amplitude of the LPF 3 are corrected, in addition to the same effect of the embodiment 1 in which, owing to the voltmeter 4 connected to the output of the LPF 3, an error occurring due to numeric irregularities of the D/A conversion element, such as a resistor constituting the D/A converter 2 are corrected.

**[0031]** The voltmeter 4 was described as a part connected to the LPF 3 and microcomputer 1. The voltmeter 4 may also be so provided that, after the D/A conversion voltage is measured during the generation of the desired D/A input data, the voltmeter 4 is released from the LPF 3 and microcomputer 1. The voltmeter 4 may be provided only when the D/A conversion voltage is measured, and, after the measurement finishes, the voltmeter may be removed from a product.

## Embodiment 3

**[0032]** Fig. 6 shows a block diagram of the waveform generation method in the embodiment 3 for carrying out this invention. In the above-described embodiment 1, the microcomputer 1 and D/A converter 2 are formed in one body. In the embodiment 3, a microcomputer 7 and a voltmeter 8 constitute an IC tester 9 so that the D/A conversion data of the D/A converter 2 are measured with the D/A converter 2 by itself.

**[0033]** The operation of this embodiment 3 is identical with that of the embodiment 1. The measured D/A conversion data are stored in advance in the internal memory of the microcomputer 1 and used in which the D/A converter 2 is incorporated separately in one body. Thus, when the D/A conversion data of the D/A converter 2 are measured with the D/A converter 2 itself in the IC tester 9, the data measurement can be conducted efficiently, so that a productivity can be improved.

## Embodiment 4

**[0034]** Fig. 7 shows a block diagram of a FMCW radar device in the embodiment 4 for carrying out this invention. In Fig. 7, desired D/A data are provided to a D/A converter 2 in accordance with a desired waveform stored in a memory (not shown) contained in a microcomputer 10 in order, to generate D/A conversion voltage D/A-converted in accordance with the desired waveform. This D/A conversion voltage is passed through a low-pass filter LPF 3, and a LPF output signal with a high-frequency component, which occurs during the D/A conversion operation, removed is obtained. When this LPF output signal is provided to a voltage control oscillator (which will hereinafter be referred to as VCO) 11 as an oscillating means, a high-frequency signal varying an oscillating frequency in accordance with the desired waveform is generated. This high-frequency signal is distributed by a directional coupler 12, and sent to a transmission antenna 13 and a mixer 14.

**[0035]** The transmission antenna 13 radiates the high-frequency signal as a transmission wave ahead of the radar device. When an object exists ahead of the radar device, the high-frequency signal is reflected on the object, and the reflected wave which is time delayed is received as a reception wave by a reception antenna 15, and sent to the mixer 14.

**[0036]** The mixer 14 generates a signal (which will hereinafter be referred to as a beat signal) representing a difference between the frequency of the reflected wave and that of the transmission wave distributed by the directional coupler 12. This beat signal is amplified in an amplifier 16, thereafter digitized in a A/D converter 17, and processed in the microcomputer to calculate a relative distance between the radar device and an object and a relative velocity thereof with respect to the object.

**[0037]** When the desired D/A input data are generated, the microcomputer 10 uses the D/A converter 2 and voltmeter 4 to be operated in the same manner as that in the embodiment 1. The voltmeter 4 shown in Fig. 7 may be provided only when the FMCW radar device is tested and regulated, and may not be incorporated in the FMCW radar device.

**[0038]** Fig. 8 shows a frequency variation diagram for calculating the relative distance between the radar device and an object, and the relative velocity thereof with respect to the object. In Fig. 8, the lateral axis represents time, and the longitudinal axis represents the frequency.

**[0039]** The transmission wave is radiated from the transmission antenna 13 so that its transmission frequency 18 is linearly increased in an UP Chirp section T<sub>mu</sub>, and linearly decreased in a DOWN Chirp section T<sub>md</sub>.

**[0040]** Where V represents the relative velocity of the radar device with respect to the object, R the relative distance between the radar device and the object, C the velocity of light,  $\lambda$  transmission wavelength,  $\Delta f$  a frequency modulation width, and T<sub>mu</sub>=T<sub>md</sub>=T<sub>m</sub>, a Doppler frequency f<sub>d</sub> is expressed by the equation (2). A distance frequency f<sub>r</sub> which is proportional to the distance and occurs due to a time difference between a transmission frequency 18 and a reception frequency 19 is expressed by the equation (3), a beat frequency f<sub>b1</sub> in the UP Chirp section T<sub>mu</sub> by the equation (4), and a beat frequency f<sub>b2</sub> in the DOWN Chirp section by the equation (5).

$$f_d = 2 \cdot V / \lambda \quad \dots (2)$$

$$f_r = (2R \cdot \Delta f) / (C \cdot T_m) \quad \dots (3)$$

$$f_{b1} = |f_d - f_r| \quad \dots (4)$$

$$f_{b2} = |f_d + f_r| \quad \dots (5)$$

5 [0041] When the distance frequency  $f_r$  is larger than the Doppler frequency  $f_d$ , the equation (6) is established.

$$2f_r = f_{b1} + f_{b2} \quad \dots (6)$$

10 [0042] When the equation (3) is substituted for the equation (6), the equation (7) which determines the relative distance  $R$  between the radar device and the object is lead out.

$$15 \quad R(C \cdot T_m) = (f_{b1} + f_{b2}) / (4 \cdot \Delta f) \quad \dots (7)$$

The distance between the radar device and the object can be determined by the beat frequency  $f_{b1}$  and beat frequency  $f_{b2}$  in the equation (7). When the distance frequency  $f_r$  is calculated, the relative velocity can also be determined by the equations (2), (4) and (5).

20 [0043] Here, the relation between a control voltage added to VCO of the FMCW radar device and an oscillation frequency is non-linear. Each VCO has an individual difference, so that it is necessary to add a control voltage to VCO in accordance with the characteristics thereof. Fig. 9 is a waveform diagram of a desired waveform 20 set in advance so as to correct the non-linearity mentioned above. Transmission frequency 18 is linearly increased or linearly decreased as mentioned above. However, since the transmission frequency has a non-linearity, a control voltage added to VCO does not become linear but becomes a high-order curve as shown by a desired waveform signal 20. The generation of desired D/A input data provided to the D/A converter 2 correspondingly to the desired waveform signal 20 can be carried out in the same manner as that for the desired waveform signal 6 in the embodiment 1.

30 [0044] The voltmeter 4 was described as a part connected to the D/A converter 2 and microcomputer 10. The voltmeter 4 may be provided so that the voltmeter be released from the D/A converter 2 and microcomputer 10 after the measurement of the D/A conversion voltage is measured during the generation of the desired D/A input data. The voltmeter 4 may be provided only when the D/A conversion voltage is measured, and may be removed from the FMCW radar device after the measurement finishes being conducted.

[0045] The voltmeter 4 may be made of an A/D converter.

35 [0046] The desired waveform signal 20 may be increased and decreased within the range, in which the DA converter 2 can practice the conversion operation, in the same manner as the desired waveform signal 6 in the embodiment 1. The number of bits of the D/A converter 2 may also be other than 8 bits at which the D/A conversion voltage is converted into 256 stages. An arbitrary D/A conversion voltage range may be set, for example, to a range having substantially equal to widths around  $V_{128}$  expressed as  $V_{128} \pm \Delta V$ , or  $V_{100}$  to  $V_{200}$  etc.

40 [0047] Thus, according to this embodiment, a D/A-converted voltage in accordance with a desired voltage is generated on the basis of the D/A conversion data obtained by varying the input data into the D/A converter 2, by a minimum conversion unit or a unit obtained by multiplying the minimum conversion unit by an integer. It thereby becomes possible to generate a D/A conversion voltage in which an error occurring due to numeric irregularities of the D/A conversion element, such as a resistor etc. constituting the D/A converter 2 was corrected.

45 [0048] Since an error occurring due to numeric irregularities of the D/A conversion element, such as a resistor, etc. constituting the D/A converter 2 can be corrected, the accuracy of the oscillating frequency of VCO is improved, so that the measuring accuracy concerning a distance between the radar device and an object, or a relative velocity thereof with respect to the object can be improved.

50 [0049] Since an error occurring due to numeric irregularities of the D/A conversion element, such as a resistor, etc. constituting the D/A converter 2 can be corrected, it becomes possible to secure a necessary accuracy without employing a high bit number expensive D/A converter, and manufacture the radar device inexpensively.

[0050] It is a matter of course that the microcomputer 10, VCO 11, LPF 3 and D/A converter 2 be formed in a package separate from that for the transmission antenna 13 and reception antenna 15.

55 Industrial Applicability

[0051] Thus, this invention can improve the accuracy of the waveform generated even when the D/A conversion element, such as a resistor constituting the D/A converter has numeric irregularities thereof. Therefore, the invention is

applied to a technical field of a waveform generation method and a radar device using a D/A converter.

## Claims

1. A waveform generation method **characterized in that** input data into a D/A converter are provided to the D/A converter in order at a timing at which a voltage of a desired waveform which has D/A conversion data indicating a conversion amount of the input data obtained by varying the input data by a minimum conversion unit or a unit obtained by multiplying the minimum conversion unit by an integer, and which varies with time series, becomes substantially equal to a D/A-converted voltage, whereby the D/A-converted voltage is generated in accordance with the desired waveform.
2. A waveform generation method according to claim 1, wherein, when the D/A conversion data are obtained, a voltage measuring means is connected to the D/A-converted voltage-generating portion to measure the D/A converted voltage and after obtaining the D/A conversion data, the voltage measuring means is then released from the D/A-converted voltage-generating portion.
3. Radar device **characterized in that** the radar device includes a storage means for storing a timing at which a D/A-converted voltage in D/A conversion data obtained by varying input data into a D/A converter by a minimum conversion unit or by a unit obtained by multiplying the minimum conversion unit by an integer and a voltage of a desired waveform varying with time series become substantially equal to each other, a waveform storage means for registering the input data at the timing, a time control means for giving the input data to the D/A converter at the timing, and an oscillation means for varying an oscillation frequency in accordance with a variation of the D/A-converted voltage.
4. Radar device according to claim 3, wherein the desired waveform is set to a waveform which renders constant a rate of change of the oscillation frequency varying with time series.
5. An oscillator for radar device **characterized in that** the oscillator includes a time storage means for storing correspondingly to input data in a D/A converter a timing at which a D/A conversion voltage in D/A conversion data obtained by varying the input data by a minimum conversion unit or by a unit obtained by multiplying the minimum conversion unit by an integer and a voltage of a desired waveform varying with time series become substantially equal to each other, a time control means for providing the input data to the D/A converter at the timing, and an oscillation means for varying an oscillation frequency in accordance with the variation of the D/A conversion voltage.



FIG. 1

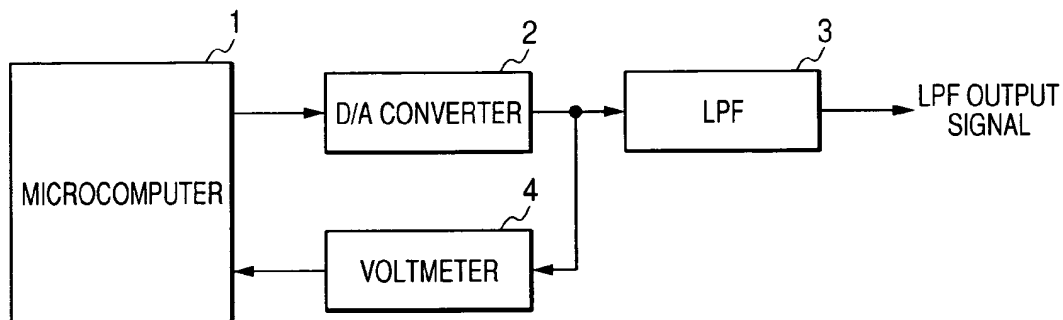


FIG. 2

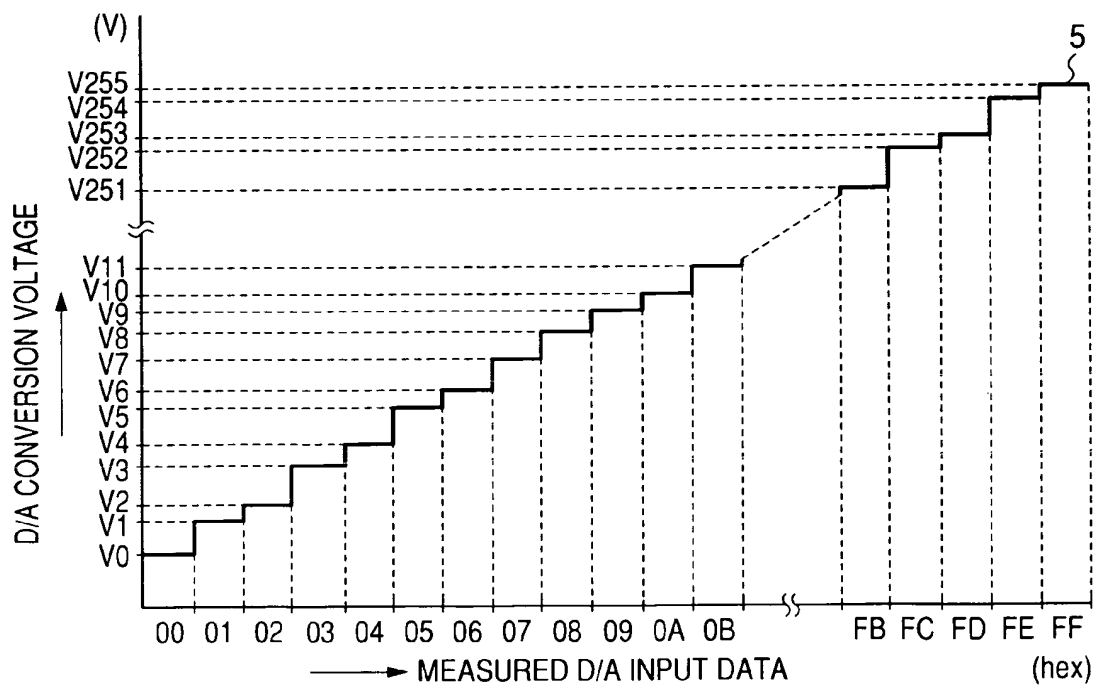
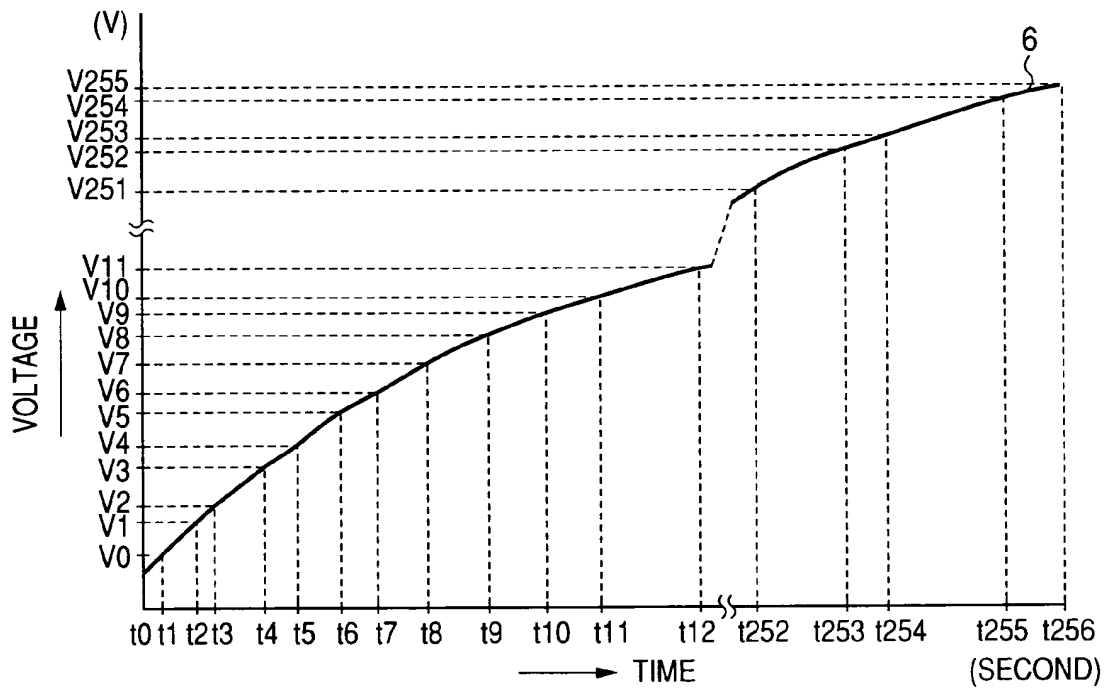


FIG. 3



**FIG. 4**

STORAGE DATA IN THE MICROCOMPUTER	
TIMING	DESIRED D/A INPUT DATA
t1	00
t2	01
t3	02
t4	03
t5	04
t6	05
t7	06
t8	07
t9	08
t10	09
t11	0A
t12	0B
⋮	⋮
t252	FB
t253	FC
t254	FD
t255	FE
t256	FF

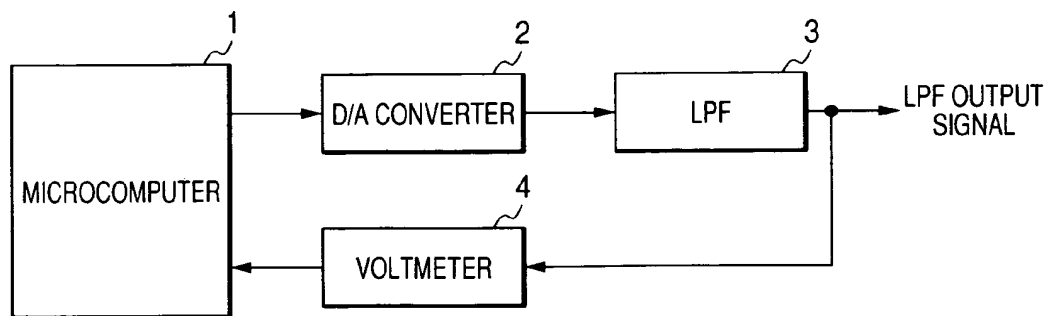
**FIG. 5**

FIG. 6

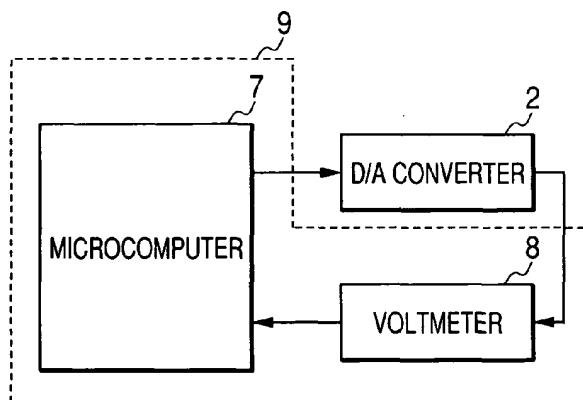
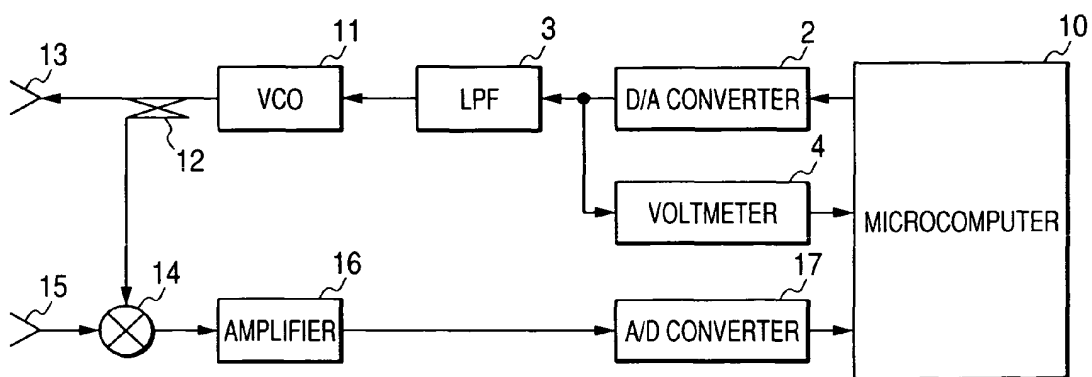
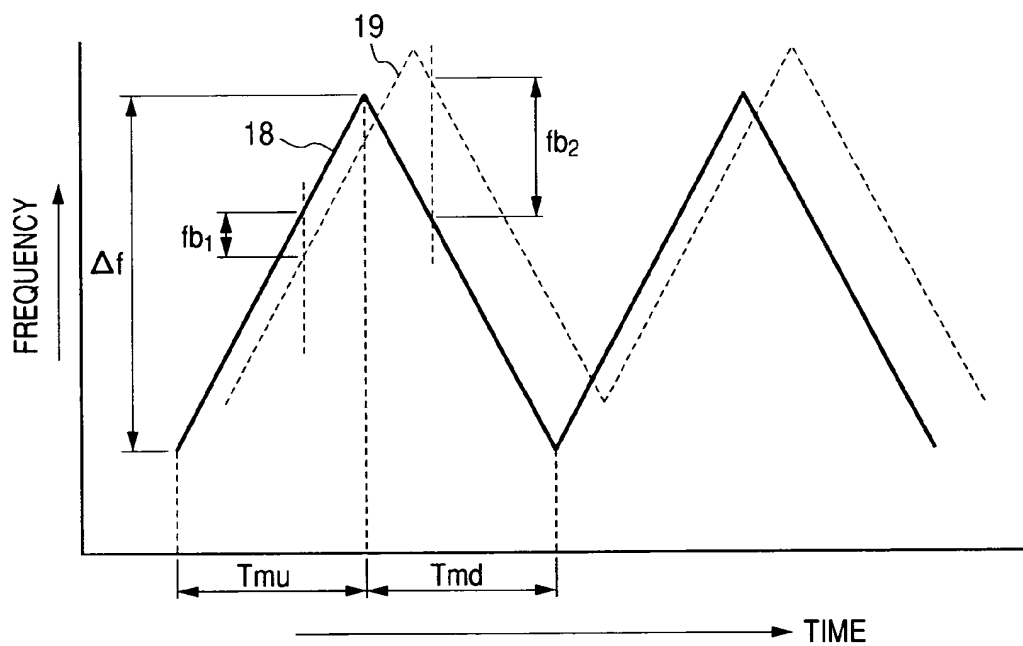


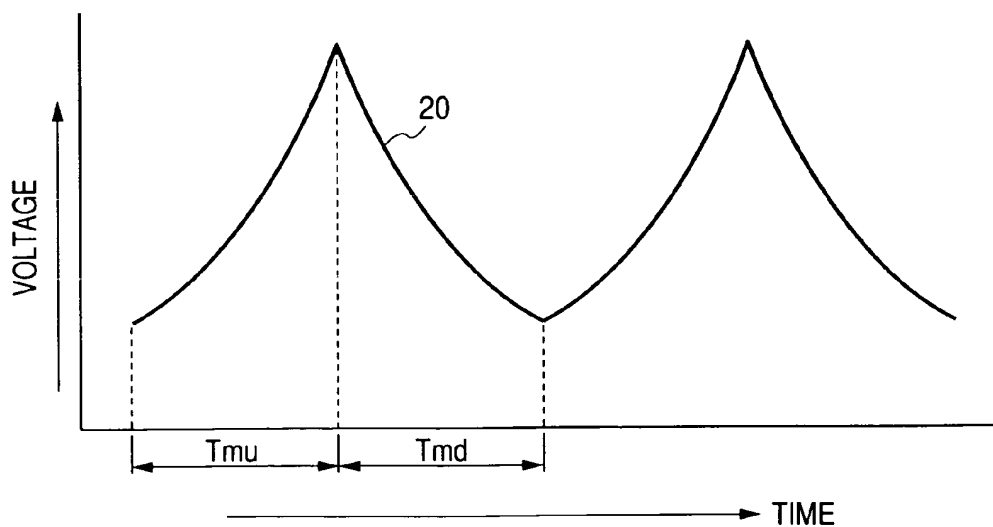
FIG. 7



**FIG. 8**



**FIG. 9**



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2005/002911

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> Int.Cl <sup>7</sup> H03M1/10, G01S7/03, 7/282, 13/34, H03B28/00  According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) Int.Cl <sup>7</sup> H03M1/10, G01S7/03, 7/282, 13/34, H03B28/00  Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1922-1996 Jitsuyo Shinan Toroku Koho 1996-2005 Kokai Jitsuyo Shinan Koho 1971-2005 Toroku Jitsuyo Shinan Koho 1994-2005  Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 03-088504 A (Yokogawa Electric Corp.), 12 April, 1991 (12.04.91), Page 2, lower right column, lines 13 to 19; Figs. 1, 3, 4 (Family: none)	1-5
A	Microfilm of the specification and drawings annexed to the request of Japanese Utility Model Application No. 42717/1989 (Laid-open No. 134723/1990) (Yokogawa Electric Corp.), 08 November, 1990 (08.11.90), Full text; all drawings (Family: none)	1-5
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search 27 May, 2005 (27.05.05)		Date of mailing of the international search report 14 June, 2005 (14.06.05)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

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## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2005/002911

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 61-144930 A (Hitachi, Ltd.), 02 July, 1986 (02.07.86), Full text; all drawings (Family: none)	1-5
A	JP 11-168325 A (Sony Corp.), 22 June, 1999 (22.06.99), Column 1, line 50 to column 2, line 24; column 6, lines 31 to 48; column 12, line 34 to column 14, line 39; column 15, line 26 to column 16, line 9; Figs. 5, 6 (Family: none)	1-5

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**REFERENCES CITED IN THE DESCRIPTION**

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**Patent documents cited in the description**

- JP 61144930 A [0003]
- JP 2002156447 A [0003]



**PUB-NO:** EP001739841A1  
**DOCUMENT-IDENTIFIER:** EP 1739841 A1  
**TITLE:** WAVEFORM GENERATION METHOD,  
RADAR DEVICE, AND OSCILLATOR  
FOR RADAR DEVICE  
**PUBN-DATE:** January 3, 2007

**INVENTOR-INFORMATION:**

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**APPL-NO:** EP05719419  
**APPL-DATE:** February 23, 2005

**PRIORITY-DATA:** JP2004049586A (February 25, 2004)

**INT-CL (IPC):** G01S007/40

**EUR-CL (EPC):** G01S007/40 , G06G007/26